

## ABSTRACT

### A VIDEO DECODING DEVICE

5           A video decoding circuit comprising: a first video data processor; a second  
video data processor; and a connection connecting the first video data processor  
and the second data processor; wherein the first video data processor is arranged to  
receive a first signal comprising encoded video data, process the first signal to  
provide a second signal and output the second signal. The first video data processor  
10   being arranged to process the first signal dependent on at least part of the received  
first signal. The second video data processor is arranged to receive at least a part of  
the second signal, process the at least a part of the second signal to provide a third  
signal, and output the third signal, the second and third signals comprising a  
decoded video image stream. The second video data processor is arranged to  
15   process the at least part of the second signal dependent on at least part of the at  
least part of second signal.